

This Datasheet for the

IC693CPU351

CPU 351 Module, (240 Kbytes Configurable User Memory, 4K I/O, 8 Racks), 2 Built-In Serial Ports, .22 msec/K

http://www.qualitrol.com/shop/p-14635-ic693cpu351.aspx

Provides the wiring diagrams and installation guidelines for this GE Series 90-30 module.

For further information, please contact Qualitrol Technical Support at

1-800-784-9385

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CPU351

Catalog Number IC693CPU351

| CPU Type | Single slot CPU module | |
|--|--|---------------|
| Total Baseplates per System | 8 (CPU baseplate + 7 expansion and/or remote) | |
| Load Required from Power Supply | 890 milliamps from +5 VDC supply | CPU 351 OND |
| Processor Speed | 25 MegaHertz | SNP P1 |
| Processor Type | 80386EX | P2 |
| Operating Temperature | 0 to 60 degrees C (32 to 140 degrees F) ambient | |
| Typical Scan Rate | 0.22 milliseconds per 1K of logic (boolean contacts) | |
| User Program Memory (maximum) | Starting with firmware Release 9.0, 240K Bytes. Note: Actual size of available user program memory depends on the amounts configured for the %R, %AI, and %AQ configurable word memory types (see below). For firmware prior to Release 9.0, fixed size is 80K bytes. | OFF/ STOP |
| Discrete Input Points - %I | 2,048 | PORT 1 RS-232 |
| Discrete Output Points - %Q | 2,048 | |
| Discrete Global Memory - %G | 1,280 bits | |
| Internal Coils - %M | 4,096 bits | |
| Output (Temporary) Coils - %T | 256 bits | PORT 2 RS-485 |
| System Status References - %S | 128 bits (%S, %SA, %SB, %SC - 32 bits each) | |
| Register Memory - %R | Starting with firmware Release 9.0, configurable in 128 word increments from 128 to 16,384 word with Logicmaster and from 128 to 32,640 words with Control version 2.2. For firmware prior to Release 9.0, fixed size is 9,999 words. | |
| Analog Inputs - %AI | Starting with firmware Release 9.0, configurable in 128 word increments from 128 to 16,384 word with Logicmaster and from 128 to 32,640 words with Control version 2.2. For firmware prior to Release 9.0, fixed size is 9,999 words. | Model 351 CPU |
| Analog Outputs - %AQ | Starting with firmware Release 9.0, configurable in 128 word increments from 128 to 16,384 word with Logicmaster and from 128 to 32,640 words with Control version 2.2. For firmware prior to Release 9.0, fixed size is 9,999 words. | |
| System Registers (for reference table viewing only; cannot be referenced in logic program) | 28 words (%SR) | |
| Timers/Counters | >2,000 | |
| Shift Registers | Yes | |
| Built-in Serial Ports | Three ports. Supports SNP/SNPX slave (on power supply connector) and RTU slave, SNP/SNPX master/slave, Serial I/O (on Ports 1 and 2). Requires CMM module for CCM; PCM module for RTU master support. | |
| Communications | LAN - Supports multidrop. Also supports Ethernet, FIP, Profibus, GBC, GCM, GCM+ option modules. | |
| Override | Yes | |
| Battery Backed Clock | Yes | |
| Interrupt Support | Supports the Periodic Subroutine feature. | |
| Type of Memory Storage | RAM and Flash | |
| PCM/CCM Compatibility | Yes | |
| Floating Point Math Support | Yes, firmware-based in firmware Release 9.0 and later. | |

The CPU351 is supported by Logicmaster 90-30/20/Micro programming and configuration software Release 6.00 and later releases, and Control programming and configuration software Release 2.0 and later releases.

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